



Search Results

Results for "((simulator <near/3> (stop* <or> halt* <or> interrupt*)) <in> pdfdata<and..."

Your search matched 2 of 219 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#) | [Cart](#) | [Search](#) | [Help](#)

Welcome - United States Patent and Trademark Office

Celebrating 150 Years
of Protecting the Future



[BROWSE](#)

[SEARCH](#)

[IEEE Xplore® Resource](#)

[Support](#)

[e-mail](#) [Printer Friendly](#)

New (Beta)
Application Notes
ISSN 1089-7986
GLOBAL INSPEC

[View Session History](#)

[New Search](#)

[Logout](#)

[Open access content](#)

[IEEE JNL](#) [Books](#) [Educational Courses](#) [Application Notes \[Beta\]](#)

[IEEE JNL](#) IEEE Journal or Magazine

[IET JNL](#) IET Journal or Magazine

[IEEE CNF](#) IEEE Conference Proceeding

[IET CNF](#) IET Conference Proceeding

[IEEE STD](#) IEEE Standard

[IEEE/NET](#)

[View Selected Items](#) [Select All](#) [Deselect All](#)

[Search](#)

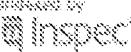
1. Scheduling optimization on the Simbus backplane
Martin, D.E.; Wilsey, P.A.; Hoekstra, R.J.; Keiter, E.R.; Hutchinson, S.A.; Russo, T.V.; Waters, L.J.;
Simulation Symposium, 2004. Proceedings. 37th Annual
18-22 April 2004 Page(s):231 - 237
Digital Object Identifier 10.1109/SIMSYM.2004.1299487
[AbstractPlus](#) | Full Text: [PDF\(1346 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

2. A multimodel approach to reasoning and simulation
Fishwick, P.A.; Narayanan, N.H.; Sticklen, J.; Bonarini, A.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 24, Issue 10, Oct. 1994 Page(s):1433 - 1449
Digital Object Identifier 10.1109/21.310527
[AbstractPlus](#) | Full Text: [PDF\(1772 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2009 IEEE -- All Rights Reserved

Indexed by





Search Results

Results for "((simulator <near/3> (stop* <or> halt* <or> interrupt*)) <in> pdfdata<and..."

Your search matched **5 of 219** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

Home | Login | Logout | Access Information | Alerts | Purchase History | Cart | Search | Help

Welcome - United States Patent and Trademark Office | Celebrating 150 Years of Progressing the Future | IEEE

BROWSE | SEARCH | IEEE EXPLORE SOURCE | SUPPORT

[e-mail](#) [Printer Friendly](#)

New (Beta)
Application Notes

GLOBAL SPEC

Modify Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

IEEE/NET

Books

Educational Courses

Application Notes [Beta]

IEEE/IET journals, transactions, letters, magazines, conference proceedings, and standards.

[View Session History](#)

[New Search](#)

 Open access content

[IEEE JNL](#) IEEE Journal or Magazine

[IET JNL](#) IET Journal or Magazine

[IEEE CNF](#) IEEE Conference Proceeding

[IET CNF](#) IET Conference Proceeding

[IEEE STD](#) IEEE Standard

[view selected items](#)

[Select All](#) [Deselect All](#)

1. Fast hardware-software coverification by optimistic execution of real processor
 Sungjoo Yoo; Jong-Eun Lee; Jinyong Jung; Kyungseok Rha; Youngchul Cho; Kiyoung Choi;
 Design, Automation and Test in Europe Conference and Exhibition 2000, Proceedings
 27-30 March 2000 Page(s):663 - 668
 Digital Object Identifier 10.1109/DATE.2000.840857
[AbstractPlus](#) | Full Text: [PDF\(88 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

2. Combined data-driven and event-driven scheduling technique for fast distributed cosimulation
 Dohyung Kim; Chan-Eun Rhee; Soonhoi Ha;
 Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
 Volume 10, Issue 5, Oct. 2002 Page(s):672 - 678
 Digital Object Identifier 10.1109/TVLSI.2002.801572
[AbstractPlus](#) | Full Text: [PDF\(1183 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

3. Fast and Accurate Cosimulation of MPSoC Using Trace-Driven Virtual Synchronization
 Youngmin Yi; Dohyung Kim; Soonhoi Ha;
 Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
 Volume 26, Issue 12, Dec. 2007 Page(s):2186 - 2200
 Digital Object Identifier 10.1109/TCAD.2007.907048
[AbstractPlus](#) | Full Text: [PDF\(357 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

4. Virtual synchronization technique with OS modeling for fast and time-accurate cosimulation
 Youngmin Yi; Dohyung Kim; Soonhoi Ha;
 Hardware/Software Codesign and System Synthesis, 2003, First IEEE/ACM/IFIP International
 Conference on
 1-3 Oct. 2003 Page(s):1 - 6
[AbstractPlus](#) | Full Text: [PDF\(527 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)



5. Performance improvement of multi-processor systems cosimulation based on SW analysis

Jinyong Jung; Sungjoo Yoo; Kiyoung Choi;

Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proceedings

13-16 March 2001 Page(s):749 - 753

Digital Object Identifier 10.1109/DATE.2001.915112

[AbstractPlus](#) | [Full Text: PDF\(420 KB\)](#) | [IEEE Xplore](#)

[Rights and Permissions](#)

[Help](#) | [Contact Us](#) | [Privacy & Security](#) | [IEEE.org](#)

Indexed by
 **Inspec®**

© Copyright 2009 IEEE - All Rights Reserved



Search Results

Results for "((simulator <near/3> (stop* <or> halt* <or> interrupt*)) <in> pdfdata<and...>"

Please verify that your query does not contain special characters, or operators without keywords.
Edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#) | [Cart](#) | [Search](#) | [Help](#)

BROWSE
SEARCH
IEEE Xplore®
SUPPORT

[e-mail](#)
[Printer friendly](#)

New (Beta)
Application Notes

GLOBAL SPEC

Modify Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

IEEE/IET
Books
Educational Courses
Application Notes [Beta]

IEEE/IET journals, transactions, letters, magazines, conference proceedings, and standards.

Open access content
 IEEE JNL IEEE Journal or Magazine
 IET JNL IET Journal or Magazine
 IEEE CNF IEEE Conference Proceeding
 IET CNF IET Conference Proceeding
 IEEE STD IEEE Standard

view selected items
[Select All](#)
[Deselect All](#)

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

[Help](#) | [Contact Us](#) | [Privacy & Security](#) | [IEEE.org](#)



Search Results

Results for "(inaccessible <near/5> hdl) <in> pdfdata"

Your search matched **0** of **2051532** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#) | [Cart](#) | [Search](#) | [Help](#)

BROWSE
SEARCH
IEEE Xplore® SOURCE
SUPPORT

[e-mail](#)
[Printer friendly](#)

New (Beta)
Application Notes

SEARCHED BY: GLOBALSPEC

Modify Search

(inaccessible <near/5> hdl) <in> pdfdata

Check to search only within this results set

Display Format: Citation Citation & Abstract

IEEE/NET Books Educational Courses Application Notes [Beta]

IEEE/ET journals, transactions, letters, magazines, conference proceedings, and standards.

[view selected items](#) [Select All](#) [Deselect All](#)

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)



Search Results

Results for "((access <near/5> hdl) <in> pdfdata<and>(simulator <near/5> (interrupt* <...>))"

Your search matched 13 of 57 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

Home | Login | Logout | Access Information | Alerts | Purchase History | Cart | Search | Help

Welcome - United States Patent and Trademark Office | Celebrating 150 Years of Protecting the Future | IEEE

BROWSE | SEARCH | IEEE EXPLORE SOURCE | SUPPORT

[e-mail](#) [Printer Friendly](#)

New [Beta]
Application Notes
GLOBAL SPEC

[View Session History](#)
[New Search](#)

IEEE
Open access content
IEEE JNL IEEE Journal or Magazine
IET JNL IET Journal or Magazine
IEEE CNF IEEE Conference Proceeding
IET CNF IET Conference Proceeding
IEEE STD IEEE Standard

Modify Search

[Search](#)

Check to search only within this results set

Display Format: Citation Citation & Abstract

[IEEE/NET](#) [Books](#) [Educational Courses](#) [Application Notes \[Beta\]](#)

Interactive online content developed from IEEE conference tutorials.

[View Selected Items](#) [Select All](#) [Deselect All](#)

- 1. **Draft Standard for the Functional Verification Language 'e' (Revision of IEEE 1647-2006)**
2008
[AbstractPlus](#) | Full Text: [PDF\(4671 KB\)](#) IEEE STD
- 2. **IEEE Standard for the Functional Verification Language e**
Aug. 3 2008 Page(s):c1 - 464
Digital Object Identifier 10.1109/IEEESTD.2008.4586409
[AbstractPlus](#) | Full Text: [PDF\(4230 KB\)](#) IEEE STD
- 3. **Draft Standard for the Functional Verification Language 'e'**
2005
[AbstractPlus](#) | Full Text: [PDF\(1618 KB\)](#) IEEE STD
- 4. **IEEE standard Verilog hardware description language**
2001 Page(s):0_1 - 856
[AbstractPlus](#) | Full Text: [PDF\(3773 KB\)](#) IEEE STD
- 5. **IEEE standard hardware description language based on the Verilog(R) hardware description language**
14 Oct. 1996
[AbstractPlus](#) | Full Text: [PDF\(6360 KB\)](#) IEEE STD
- 6. **Unapproved IEEE Draft Standard for the Functional Verification Language 'e' (Replaced by the approved draft)**
2005
[AbstractPlus](#) | Full Text: [PDF\(1618 KB\)](#) IEEE STD
- 7. **IEEE Unapproved Draft Standard for Verilog? Hardware Description Language (Revision of 1364-1995) Replaced by approved draft**
2005
[AbstractPlus](#) | Full Text: [PDF\(3757 KB\)](#) IEEE STD

- 8. **IEEE Approved Draft Standard for Verilog® Hardware Description Language Superseded by 1364-2005**
5
[AbstractPlus](#) | Full Text: [PDF\(3755 KB\)](#) IEEE STD

- 9. **IEEE Standard for the Functional Verification Language 'e'**
2006 Page(s):0_1 - 371
[AbstractPlus](#) | Full Text: [PDF\(2963 KB\)](#) IEEE STD

- 10. **IEEE Std 1364 -2005 IEEE Standard for Verilog Hardware Description Language**
2006 Page(s):0_1 - 560
[AbstractPlus](#) | Full Text: [PDF\(5970 KB\)](#) IEEE STD

- 11. **Behavioural languages - Part 4: Verilog hardware description language**
2004 Page(s):0_1 - 860
[AbstractPlus](#) | Full Text: [PDF\(4955 KB\)](#) IEEE STD

- 12. **Fast and Accurate Cosimulation of MPSoC Using Trace-Driven Virtual Synchronization**
Youngmin Yi; Dohyung Kim; Soonhoi Ha;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)
Volume 26, [Issue 12](#), Dec. 2007 Page(s):2186 - 2200
Digital Object Identifier 10.1109/TCAD.2007.907048
[AbstractPlus](#) | Full Text: [PDF\(357 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- 13. **Timed compiled-code functional simulation of embedded software for performance analysis of SOC design**
Jong-Yeol Lee; In-Cheol Park;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)
Volume 22, [Issue 1](#), Jan. 2003 Page(s):1 - 14
Digital Object Identifier 10.1109/TCAD.2002.805721
[AbstractPlus](#) | Full Text: [PDF\(1377 KB\)](#) IEEE JNL
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2009 IEEE - All Rights Reserved